

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/982,459	SCHMITT ET AL.	
Examiner		Art Unit		Page 1 of 1
A. M. Thompson		2825		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,457,159	09-2002	Yalcin et al.	716/6
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages
	U	P. Larsson et al., Impact of Clock Slope on True Single Phase Clocked (TSPC) CMOS Circuits, IEEE Journal of Solid-State Circuits, pages 723-726, June 1994.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.